Optimized ASIP Synthesis from Architecture Description Language Models

Oliver Schliebusch, Heinrich Meyr, Rainer Leupers

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New software tools and a sophisticated methodology above RTL are required to answer the challenges of designing an optimized application specific processor (ASIP). This book offers an automated and fully integrated implementation flow and compares it to common implementation practice. It provides case-studies that emphasize that neither the architectural advantages nor the design space of ASIPs are sacrificed for an automated implementation.

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